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1 KDB: a multi-threaded debugger for multi-threaded applications   ■
Petèr A. Buhr, Martin Karsten, Jun Shih
January 1996 **Proceedings of the SIGMETRICS symposium on Parallel and distributed tools**
Full text available: 📄 pdf(991.10 KB)   Additional Information: full citation, references, citings, index terms

2 The mach exception handling facility   ▰
David L. Black, David B. Golub, Karl Hauth, Avadis Tevanian, Richard Sanzi
November 1988 **ACM SIGPLAN Notices , Proceedings of the 1988 ACM SIGPLAN and SIGOPS workshop on Parallel and distributed debugging**, Volume 24 Issue 1
Full text available: 📄 pdf(1.18 MB)   Additional Information: full citation, references, citings, index terms

3 Space-efficient scheduling of parallelism with synchronization variables   ▬
Guy E. Blelloch, Phillip B. Gibbons, Girija J. Narlikar, Yossi Matias
June 1997 **Proceedings of the ninth annual ACM symposium on Parallel algorithms and architectures**
Full text available: 📄 pdf(1.67 MB)   Additional Information: full citation, references, citings, index terms

4 Fast detection of communication patterns in distributed executions   ▬
Thomas Kunz, Michiel F. H. Seuren
November 1997 **Proceedings of the 1997 conference of the Centre for Advanced Studies on Collaborative research**
Full text available: 📄 pdf(4.21 MB)   Additional Information: full citation, abstract, references, index terms

Understanding distributed applications is a tedious and difficult task. Visualizations based on process-time diagrams are often used to obtain a better understanding of the execution of the application. The visualization tool we use is Poet, an event tracer developed at the University of Waterloo. However, these diagrams are often very complex and do not provide the user with the desired overview of the application. In our experience, such tools display repeated occurrences of non-trivial commun ...

5 A thread-aware debugger with an open interface   ▬

Daniel Schulz, Frank Mueller
August 2000 **ACM SIGSOFT Software Engineering Notes , Proceedings of the 2000 ACM SIGSOFT international symposium on Software testing and analysis**, Volume 25 Issue 5

Full text available: 📄 pdf(347.13 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

While threads have become an accepted and standardized model for expressing concurrency and exploiting parallelism for the shared-memory model, debugging threads is still poorly supported. This paper identifies challenges in debugging threads and offers solutions to them. The contributions of this paper are threefold. First, an open interface for debugging as an extension to thread implementations is proposed. Second, extensions for thread-aware debugging are identified and implemented wit ...

Keywords: active debugging, concurrency, debugging, open interface, threads

6 Fast breakpoints: design and implementation

Peter B. Kessler
June 1990 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1990 conference on Programming language design and implementation**, Volume 25 Issue 6

Full text available: 📄 pdf(855.02 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

We have designed and implemented a fast breakpoint facility. Breakpoints are usually thought of as a feature of an interactive debugger, in which case the breakpoints need not be particularly fast. In our environment breakpoints are often used for non-interactive information gathering; for example, procedure call count and statement execution count profiling [Swinehart, et al.]. When used non-interactively, breakpoints should be as fast as possible, so as to perturb the execution of the pro ...

7 ReEnact: using thread-level speculation mechanisms to debug data races in multithreaded codes

Milos Prvulovic, Josep Torrellas
May 2003 **ACM SIGARCH Computer Architecture News , Proceedings of the 30th annual international symposium on Computer architecture**, Volume 31 Issue 2

Full text available: 📄 pdf(184.86 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>

While removing software bugs consumes vast amounts of human time, hardware support for debugging in modern computers remains rudimentary. Fortunately, we show that mechanisms for Thread-Level Speculation (TLS) can be reused to boost debugging productivity. Most notably, TLS's rollback capabilities can be extended to support rolling back recent buggy execution and repeating it as·many times as necessary until the bug is fully characterized. These incremental re-executions are deterministic even i ...

8 The Howitzer improvement program: lessons learned

D. Krantz
January 1989 **Proceedings of the conference on Tri-Ada '89: Ada technology in context: application, development, and deployment**

Full text available: 📄 pdf(1.59 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>index terms</u>

9 Increasing effective IPC by exploiting distant parallelism

Iván Martel, Daniel Ortega, Eduard Ayguadé, Mateo Valero
May 1999 **Proceedings of the 13th international conference on Supercomputing**

Full text available: 📄 pdf(1.20 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>index terms</u>

10 Debugging concurrent programs

Charles E. McDowell, David P. Helmbold

December 1989 **ACM Computing Surveys (CSUR)**, Volume 21 Issue 4

Full text available: pdf(2.86 MB)   Additional Information: full citation, abstract, references, citings, index terms, review

The main problems associated with debugging concurrent programs are increased complexity, the "probe effect," nonrepeatability, and the lack of a synchronized global clock. The probe effect refers to the fact that any attempt to observe the behavior of a distributed system may change the behavior of that system. For some parallel programs, different executions with the same data will result in different results even without any attempt to observe the behavior. Even when the behavior can be ...

11 Migration: Luna: a flexible Java protection system

Chris Hawblitzel, Thorsten von Eicken

December 2002 **ACM SIGOPS Operating Systems Review**, Volume 36 Issue SI

Full text available: pdf(1.39 MB)   Additional Information: full citation, abstract, references

Extensible Java systems face a difficult trade-off between sharing and protection. On one hand, Java's ability to run different protection domains in a single virtual machine enables domains to share data easily and communicate without address space switches. On the other hand, unrestricted sharing blurs the boundaries between protection domains, making it difficult to terminate domains and enforce restrictions on resource usage. Existing solutions to these problems restrict sharing in an ad-hoc ...

12 Improving IPC by kernel design

Jochen Liedtke

December 1993 **ACM SIGOPS Operating Systems Review , Proceedings of the fourteenth ACM symposium on Operating systems principles**, Volume 27 Issue 5

Full text available: pdf(1.39 MB)   Additional Information: full citation, abstract, references, citings, index terms

Inter-process communication (ipc) has to be fast and effective, otherwise programmers will not use remote procedure calls (RPC), multithreading and multitasking adequately. Thus ipc performance is vital for modern operating systems, especially μ-kernel based ones. Surprisingly, most μ-kernels exhibit poor ipc performance, typically requiring 100 μs for a short message transfer on a modern processor, running with 50 MHz clock rate.In contrast, we achieve 5 μs; a twenty ...

13 Source level debugging of automatically parallelized code

Robert Cohn

December 1991 **ACM SIGPLAN Notices , Proceedings of the 1991 ACM/ONR workshop on Parallel and distributed debugging**, Volume 26 Issue 12

Full text available: pdf(1.34 MB)   Additional Information: full citation, references, citings, index terms

14 Debuggable concurrency extensions for standard ML

Andrew P. Tolmach, Andrew W. Appel

December 1991 **ACM SIGPLAN Notices , Proceedings of the 1991 ACM/ONR workshop on Parallel and distributed debugging**, Volume 26 Issue 12

Full text available: pdf(1.22 MB)   Additional Information: full citation, references, citings, index terms

15 <u>Summary of the sigmetrics symposium on parallel and distributed processing</u>

Jeffrey K. Hillingsworth, Barton P. Miller

March 1999 **ACM SIGMETRICS Performance Evaluation Review**, Volume 26 Issue 4

Full text available: <u>pdf(1.17 MB)</u>   Additional Information: <u>full citation</u>, <u>index terms</u>

16 <u>Process migration</u>

September 2000 **ACM Computing Surveys (CSUR)**, Volume 32 Issue 3

Full text available: <u>pdf(1.24 MB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>, <u>review</u>

Process migration is the act of transferring a process between two machines. It enables dynamic load distribution, fault resilience, eased system administration, and data access locality. Despite these goals and ongoing research efforts, migration has not achieved widespread use. With the increasing deployment of distributed systems in general, and distributed operating systems in particular, process migration is again receiving more attention in both research and product development. As hi ...

Keywords: distributed operating systems, distributed systems, load distribution, process migration

17 <u>Transformations for model checking distributed Java programs</u>

Scott D. Stoller, Yanhong A. Liu

May 2001 **Proceedings of the 8th international SPIN workshop on Model checking of software**

Full text available: <u>pdf(108.43 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>

This paper describes three program transformations that extend the scope of model checkers for Java programs to include distributed programs, *i.e.*, multi-process programs. The transformations combine multiple processes into a single process, replace remote method invocations (RMIs) with local method invocations that simulate RMIs, and replace cryptographic operations with symbolic counterparts.

18 <u>Potpourri: Helper threads via virtual multithreading on an experimental itanium® 2 processor-based platform</u>

Perry H. Wang, Jamison D. Collins, Hong Wang, Dongkeun Kim, Bill Greene, Kai-Ming Chan, Aamir B. Yunus, Terry Sych, Stephen F. Moore, John P. Shen

October 2004 **Proceedings of the 11th international conference on Architectural support for programming languages and operating systems**

Full text available: <u>pdf(225.47 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Helper threading is a technology to accelerate a program by exploiting a processor's multithreading capability to run ``assist'' threads. Previous experiments on hyper-threaded processors have demonstrated significant speedups by using helper threads to prefetch hard-to-predict delinquent data accesses. In order to apply this technique to processors that do not have built-in hardware support for multithreading, we introduce virtual multithreading (VMT), a novel form of switch-on-event user-level ...

Keywords: DB2 database, PAL, cache miss prefetching, helper thread, itanium processor, multithreading, switch-on-event

19 A distributed debugger for Amoeba

I. J. P. Elshoff

November 1988 **ACM SIGPLAN Notices , Proceedings of the 1988 ACM SIGPLAN and SIGOPS workshop on Parallel and distributed debugging,** Volume 24 Issue 1

Full text available: pdf(1.15 MB)   Additional Information: full citation, abstract, references, citings, index terms, review

We describe a debugger that is being developed for distributed programs in Amoeba. A major goal in our work is to make the debugger independent of the Amoeba kernel. Our design integrates many facilities found in other debuggers, such as execution replay, breakpointing, and an event-based view of the execution of the target program. This paper discusses the influence of Amoeba's architecture on the attainability of our goals and the desired functionality of the debugger. We also consider su ...

20 A survey of trace exploration tools and techniques

Abdelwahab Hamou-Lhadj, Timothy C. Lethbridge

October 2004 **Proceedings of the 2004 conference of the Centre for Advanced Studies on Collaborative research**

Full text available: pdf(423.87 KB)   Additional Information: full citation, abstract, references, index terms

The analysis of large execution traces is almost impossible without efficient tool support. Lately, there has been an increase in the number of tools for analyzing traces generated from object-oriented systems. This interest has been driven by the fact that polymorphism and dynamic binding pose serious limitations to static analysis. However, most of the techniques supported by existing tools are found in the context of very specific visualization schemes, which makes them hard to reuse. It i ...

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(b)11 3.3 Linking of segments at **break-point**.
Background :11 3.4.1 **Removal** of Intersection Points :
of an edge map of the intensity image. 3.4.1 **Removal** of Intersection Points Figure 3.2 shows an
ftp.cs.umd.edu/pub/papers/papers/ncstrl.umcp/CS-TR-3176/CS-TR-3176.ps.Z

Restoration Of Images Scanned From Thick Bound Documents - Zhang, Tan (2001) (Correct) (2 citations)
side near the peak position. For each line, a **break point**, b, is found, and the boundary between shadow
Adjust The Warped Words In The Shadow. 2. Shadow **Removal** A Typical Grayscale Image For A Page Scanned
subsections describe the procedures for shadow **removal**. Figure 1. A grayscale image scanned from a
ipml.ee.duth.gr/~papamark/citation/Zhang_icip01.pdf

Equity Market Liberalization in Emerging Markets - Geert Bekaert Campbell (Correct)
additional emerging markets. The estimate of the **break point** in U.S. equity portfolio holdings is obtained
these reforms involved (for the first time) the **removal** of restrictions on foreigners holding domestic
faculty.fuqua.duke.edu/~charvey/Research/Published_Papers/P85_Equity_market_liberalization.pdf

μProfiler: Profiling User-Level Threads in a Shared-Memory.. - Buhr, Denda (Correct)
160 a target-specific debugger for **break-point** debugging on a user-level thread basis, and
methods: dynamic instrumentation insertion and **removal** based on execution-time profiling information,
plg.uwaterloo.ca/pub/MVD/ISCOPE98.ps.gz

Patternand Process Dependencies In Copper Damascene Chemical .. - Park Tugbawa Yoon (1998) (Correct)
process. Furthermore, we observe an interesting **break point** for both erosion and dishing at an oxide line
table speed and carrier speed such that the bulk **removal** rate of copper will be the same for all three
www-mtl.mit.edu/Metrology/PAPERS/PAPERS/VMIC98.ps

Reconstruction of Music Scores from Primitive Subsegmentation - Ng, Boyle (1994) (Correct)
is used instead. For methods (ii) and (iii)the **break point** is determined by the maximum value of the
Thresholding Score layout analysis Stave line **removal** Figure 2: Pre-processing stages with example
quaver groups and the result after stave line **removal**. These blocks are inspected sequentially to
agora.leeds.ac.uk/scs/doc/reports/1994/94_12.ps.Z

Use and Overuse of Neural Networks in Statistics - Mola, Davino, Siciliano.. (Correct)
associated to the validation set gives the **break-point** of the cycles in the neural network.
produces the complete patterns in output noise **removal**: the neural network produces in output a cleaned
www.ehu.es/~bszwww/ngus/papers/sicilian.ps

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An Experimental Comparison of Fault and Error Injection - Christmansson, Hiller, Rimén (1998) (Correct)
(3 citations)
M. Hiller. An Experimental Comparison of Fault **and** Error Injection"9th IEEE Proc. Int. Symp. on
www.ce.chalmers.se/staff/hiller/pubs/christmansson_issre98.pdf

AGDB: A Debugger for Agent Tcl - Melissa Hirschl (1997) (Correct) (2 citations)
Traditional debugging features include **breakpoints** (line-specific, conditional, and once-only)
debugger preprocesses the file again, resulting in **removal** of all line-specific **breakpoints**. 2.1 How the
ftp.cs.dartmouth.edu/TR/TR97-306.ps.Z

VisMod: A Beginner-Friendly Programming Environment - Ricardo Jim Enez-Peris (1999) (Correct) (1 citation)
ways of running a program: step-by-step, until a **breakpoint** or completely. In the step-by-step mode, the
run in a step-by-step mode anymore. By setting **breakpoints**, one can focus on those parts of the code of
as well as in class, arrows Figure 2: Iterative **removal** of a list represent addresses, and the object
lsd.ls.fi.upm.es/lsd/papers/1999/sac99b.ps

System Specification and Storage Architecture Exploration for.. - Moolenaar (1996) (Correct) (1 citation)
: 48 3.19 Example of data partitioning with **breakpoint** after first nondc DCT value for Intra
iDCT entry. EOB End Of Block Possible priority **breakpoint** *Switch between layers Stream Macroblock
the MPEG-2 decoder. 144 7.8.1 Implementing the **removal** of the Saturate procedure from the MPEG-2
ce.et.tudelft.nl/publicationfiles/580_332_moolenaar.ps

Debugging Using Time Machines - Replay Your Embedded Systems.. - Thane, Sundmark (2001) (Correct)
(1 citation)
classical debugging techniques such using **breakpoints** or single stepping in real-time systems change
phase, the developer needs to be able to set **breakpoints** and single-step through the execution.
This type of behavior, when the insertion and **removal** of probes affect the execution of the system, is
www.mrtc.mdh.se/publications/0347.pdf

Specification and Validation of Communications in Client/Server.. - Lin (1994) (Correct) (1 citation)
lack of support for notions like "distributed **breakpoint**" and "distributed single-step"2] This paper
of this problem. It focuses on the detection and **removal** of the communications errors in client/server
thumper.bellcore.com/pub/fjlin/cs_full.ps

Modeling Of Pattern Dependencies In Abrasive-Free - Copper Chemical Mechanical (2001) (Correct)
copper **removal** rate is approximately zero, and a **breakpoint** pressure p 2 at which the slope of the copper
with the empirical relationship between the **removal** rate and the polishing pressure, to construct
rate and the polishing pressure, to construct **removal** rate diagrams for the process. From these
www-mtl.mit.edu/Metrology/PAPERS/VMIC2001.pdf

Investigation of a New Alpha-Cut Based Fuzzy.. - Baranyi, Tikk, Yam.. (Correct)
namely, based on the characteristic points or **breakpoint** levels) of the involved sets, because otherwise
original fuzzy mapping, only its important cuts (**breakpoint** levels or sets) is approximated. Hence, the
interpolation. The use of sparse rule base allows **removal** of redundant rules with proper techniques even
www.mft.hu/publications/tikk/tikk11.pdf

Programming with CGAL: the example of triangulations - Boissonnat, Cazals, Da.. (Correct)
the programmer can stop anywhere using a **breakpoint**. Consider the situation where the programmer is
of geometric data structures upon insertion/**removal**/update of inputs. The goal of this video is
www-sop.inria.fr/prisme/publis/bcddprty-pcet-99.ps.gz

Hierarchical Scattered Data Filtering for Multilevel Interpolation .. - Iske (1999) (Correct)

X =T k Z) 2 XZ by picking one suitable **breakpoint** k AE 0 at run time (i.e. during the **removal**)
1 and this provides a useful criterion for the **breakpoint** selection. Scattered Data Filtering 215
composition of greedy thinning, a recursive point **removal** strategy, and exchange, a local optimization
www-m8.mathematik.tu-muenchen.de/m3/iske/../ftp/Oslo2000.ps

Transformations for Model Checking Distributed Java Programs - Stoller, Liu (2001)   (Correct)
jdb to debug a distributed program. jdb supports **breakpoints**, but a **breakpoint** halts only a single process.
program. jdb supports **breakpoints**, but a **breakpoint** halts only a single process. If jdb is used to
This yields a nondistributed program. RMI **removal**: replace RMIs with ordinary method invocations
netlib.bell-labs.com/netlib/spin/ws01/stoller.pdf

A Brief Overview of the MAD Debugging Activities - Kranzlmüller.. (2000)   (Correct)
arrays, the possibility to graphically place **breakpoints**, a mechanism for monitor overhead **removal**, and
source code. This is achieved with methods like **breakpoint**ing and inspection, which are applied during
breakpoints, a mechanism for monitor overhead **removal**, and the evaluation of racing messages occurring
www.irisa.fr/lande/ducasse/aadebug2000/proceedings/D4kranzlmueller.ps.gz

CLEF Experiments at the University of Maryland.. - Oard, Levow, Cabezas (2000)   (Correct)
available Linguistica package and a threshold **breakpoint** stemming technique that was developed in-house.
suffixes that would result from every possible **breakpoint**. An optimal **breakpoint** for each token is then
were mapped to lower case 7-bit ASCII through **removal** of accents. Once normalization was complete,
www.clis.umd.edu/dlrg/filter/papers/clef.ps

Thinning Algorithms In Scattered Data Modelling - Iske   (Correct)
nL nL 1 n 1 0 of reasonable **breakpoints**, which gives us by X j =T n j Z)1 j
(1.1)But the a priori selection of the **breakpoints** is, especially for the application from the
hierarchies of the form (1.1) by recursive point **removals** from Z. Likewise, the expression thinning is
www-m8.mathematik.tu-muenchen.de/m3/iske/../ftp/Nachsholim.ps

Testing Debugging - Task Leader   (Correct)
phase allows to use interactive techniques like **breakpoints**, step-by-step execution, variable inspection,
is used to control installation and **removal** of **breakpoint** traps, introduced by the scenario script, and
The debugger is used to control installation and **removal** of **breakpoint** traps, introduced by the scenario
ftp.cpc.wmin.ac.uk/pub/sepp/P6/task7.ps.gz

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Fine-Grained Dynamic Instrumentation of Commodity Operating.. - Tamches, Miller (1999) (Correct)
(30 citations)
using finegrained dynamic instrumentation. **Breakpoints** can be inserted at any machine code
reached. Instrumentation for a basic block can be **remove**d as soon as the flag is set thus, the overhead
kernel state and (optionally) pauses the executing **thread** and informs the debugger. Conditional **breakpoints**
www.cs.wisc.edu/paradyn/papers/kerninst.ps.gz

Unknown - Technology Journal Communications (2003) (Correct)
packet list, history event capture, conditional **breakpoint**, and instruction operand tracing features.
IXA software framework [3]describe extensions to **remove** such limitations and discuss how these extensions
allow debugging using bandwidth analysis, **thread** history, and operand tracing, and provide a novel
developer.intel.com/technology/itj/2003/volume07issue04/art06_tools/vol7iss4_art06.pdf

Reprinted from Proceedings of the 21st ACM Symposium on the .. - Norman Ramsey Bell (Correct)
January 1994. Correctness of Trap-Based **Breakpoint** Implementations Norman Ramsey Bell
www.eecs.harvard.edu/~nr/pubs/correctness.ps

FIRST: A Framework for Interoperable Resources, Services, and.. - Prodan, Kewley (Correct)
constructs, such as: set/increment counter, set **breakpoint** and insert timer. Extensibility: By defining
instrumentation ffl add instrumentation to (and **remove** it from) the attached application process at any
The Process Manager has been designed to be multi-**thread**ed with a separate **thread** for each of its main
www.cscs.ch/~kewley/pubs/PDPTA99.ps.gz

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muller - Re: [RFA/RFC 3] Remove hardware break and watchpoints at ...
... wrote: >Pierre Muller <muller@cerbere.u-strasbg.fr> writes: > >> After the previous
discussion about the >> hardware watch and **breakpoint removal** >> about the ...
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about the debuggee exited. I slightly modifed my patch ...
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... **thread**->component, –**thread**->pc); if (info) **thread**->advance = skip_byte_breakpoint;
mindy_pause(pause_HitBreakpoint); } /* **Breakpoint removal**. ...
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... null; IMTrace.out("Interpreter SwingWorker **thread** dying."); } mWorker ... void
actionPerformed(ActionEvent event) { IMTrace.out("**Breakpoint removal** requested on ...
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... The **breakpoint removal** was causing the former contents (a zero word) to be written
back to the now initialized PLT entry thus ... If the current **thread** is about to ...
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... find_byte_breakpoint(**thread**->component, –**thread**->pc); if (info) **thread**->advance = skip_byte_breakpoint; pause(pause_HitBreakpoint); } /* **Breakpoint removal. ...**
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<u>Atsushi Nemoto - gdbserver: **thread** getmsg err on mips[el]-linux</u>
... for **breakpoint**. **Removed breakpoint**. Got an event from 3328 (57f) Hit a
(non-reinsert) **breakpoint**. **Thread** creation event. **thread** ...
sources.redhat.com/ml/gdb/2005-01/msg00071.html - 10k - <u>Cached</u> - <u>Similar pages</u>

<u>Jon Ringle - Re: pthread_create does not return when remote ...</u>
... Writing 00 to 40111a2c Attaching to **thread** 2049 (LWP 194 ... signal 0, stop not expected)
pending reinsert at 40104d44Checking for **breakpoint**. **Removed breakpoint**. ...
sources.redhat.com/ml/gdb/2003-06/msg00528.html - 6k - <u>Cached</u> - <u>Similar pages</u>
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<u>ILE C/C++ Programmer's Guide - Setting a Conditional **Thread** ...</u>
... **breakpoint**, use the CLEAR debug command. When a conditional **thread breakpoint**
is **removed**, it is **removed** for the current **thread** only. ...
publib.boulder.ibm.com/infocenter/iadthelp/ topic/com.ibm.etools.iseries.pgmgd.doc/cpprog205.htm - 3k -
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<u>IJavaBreakpointListener (Eclipse JDT API Specification)</u>
... been hit in the specified **thread** - returns whether the **thread** should suspend. ... **breakpoint**)
Notification that the given **breakpoint** has been **removed** from the ...
publib.boulder.ibm.com/.../reference/ api/org/eclipse/jdt/debug/core/IJavaBreakpointListener.html -
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<u>org.eclipse.debug.core.model (Eclipse Platform API Specification)</u>
... for example the creation or termination of a **thread**. ... resource deltas into **breakpoint**
change notifications (**breakpoint** added/**removed**/changed messages). ...
help.eclipse.org/.../reference/api/ org/eclipse/debug/core/model/package-summary.html - 28k -
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<u>DebugListener (drjava-beta-20050110)</u>
... **removed** from a document. void, breakpointSet(**Breakpoint** bp) Called when a **breakpoint**
is set in a document. void, currThreadDied() Called when the current **thread** ...
drjava.sourceforge.net/javadoc/ edu/rice/cs/drjava/model/debug/DebugListener.html - 21k -
<u>Cached</u> - <u>Similar pages</u>

<u>NetBeans 4.0 Debugger Changes - Users View</u>
... customizer for all debugger nodes - **thread** node, session ... Properties Action for simple
items will be **removed**. ... the pop-up menu of the **breakpoint** (all **breakpoint** ...
debuggercore.netbeans.org/UI/UsersView-40Features.html - 43k - <u>Cached</u> - <u>Similar pages</u>

<u>Debugging with GDB: Stopping</u>
... is created and the original pending **breakpoint** is **removed**. ... would be to set a code

breakpoint at the ... Warning: In multi-**thread** programs, watchpoints have only ...
www.rons.net.cn/htp/gdb/gdb_6.html - 89k - <u>Cached</u> - <u>Similar pages</u>

<u>Storage Change/Watch **Breakpoint**/Watchpoint dialog box</u>
... the routine's stack frame will be **removed** from the ... this group to define where the
breakpoint is placed ... **Thread**, This selection list lets you choose what threads ...
www.nersc.gov/vendor_docs/ ibm/xlf/html/debugger/ui/ubdbscbp.htm - 17k - <u>Cached</u> - <u>Similar pages</u>

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ɔurces.redhat.com/ml/gdb-patches/2002-01/msg00331.html - 7k - Cached - More from this site

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after these changes: gdb-pre-params-**removal**-2000-05-28 gdb-post-params-**removal**-2000-05-28 ... ADDR)); ⊣
:reate_**thread**_event_**breakpoint** (CORE_ADDR); -extern void ...
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ug 1695 - DCR: support process/**thread** specific breakpoints (1GIYJTQ) ▣
void addThreadFilter(IJavaThread **thread**) - restricts **breakpoint** to given **thread** and any ... re-create **breakpoi**
e **removal** of **thread** filters) - public ...
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un Dec 19 18:56:38 1999 Andrew Cagney <cagney@b1.cygnus.com> * **breakpoint**.c (mention): Delete ui-out b⟋
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n introduction to parallel programming with OpenMP. ... word "outline", the antonym of "inline", emphasizes the r⟋
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New command remove-symbol-file allows **removal** of obsolete symbol table entries. ... SIGTRAP (rather than a
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thread_xfer_memory): Protoize. 2000-10-16 Peter Schauer <pes@regent.e-technik.tu-muenchen.de> * sparc-t(
1prove·comments. Adjust call_dummy_**breakpoint** ...
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